

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	0	(bipolar adj transistor) and (control adj5 circuit) near5 (driv\$3) near5 (insulat\$3 adj gate) and (input adj terminal) adj (via or hole or recess) and (schottky adj barrier) near5 diode and p-channel near5 short\$1	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/03/21 16:22
L23	2	("6856566").pn"	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/03/21 17:12
L24	0	2 and substrate	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/03/21 17:12
L25	10215	schottky adj barrier	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/03/21 17:12
L26	859	25 and bipolar adj transistor	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/03/21 17:12
L27	467015	26 control adj circuit	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/03/21 17:13
L28	85	26 and control adj circuit	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/03/21 17:13
L29	11	28 and p-channel with MOSFET	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/03/21 17:21
L30	1	29 and short\$1 near10 ends same schottky adj barrier	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/03/21 17:22
L31	1	29 and short\$1 same ends same schottky adj barrier	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/03/21 17:22